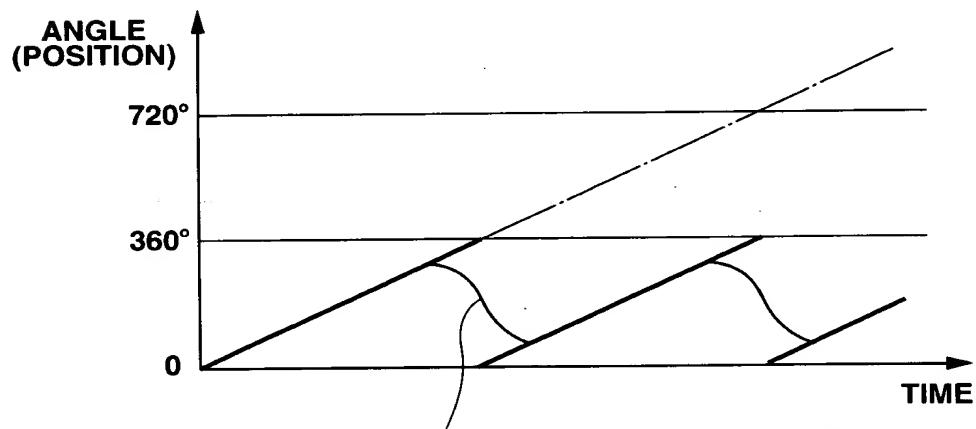


FIG.1

- LPF INPUT PHASE AND CORRECT OUTPUT PHASE (FOR A RANGE OF FROM 0° TO 360°)
- LPF INPUT PHASE AND CORRECT OUTPUT PHASE (WITHOUT LIMITATION TO RANGE)
- NORMAL LPF OUTPUT PHASE (FOR A RANGE OF FROM 0° TO 360°)



LPF OUTPUT TAKING WRONG COURSE NEAR 360°

FIG.2

0 9649539 - 082600

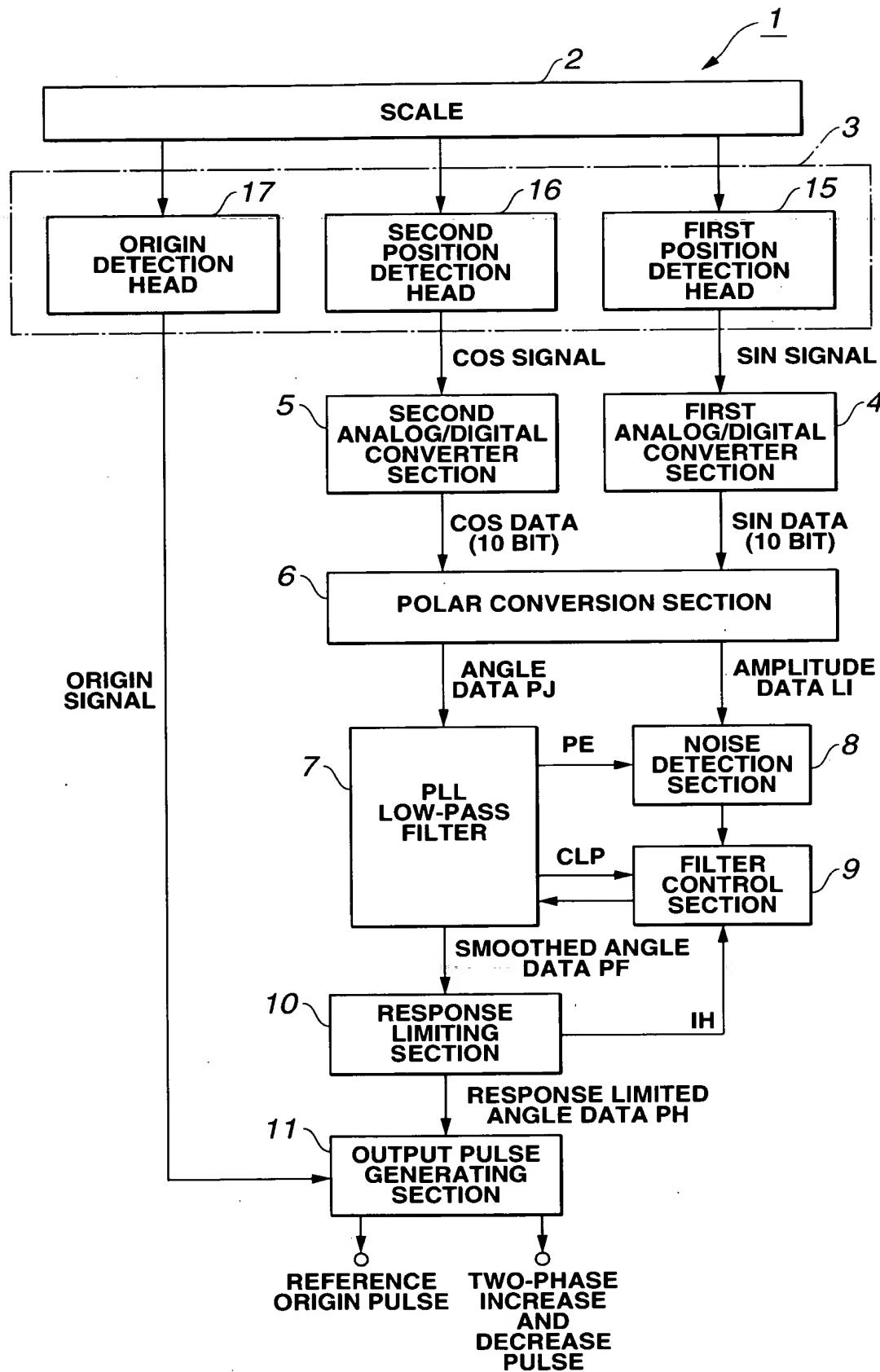


FIG.3

FIG.4A

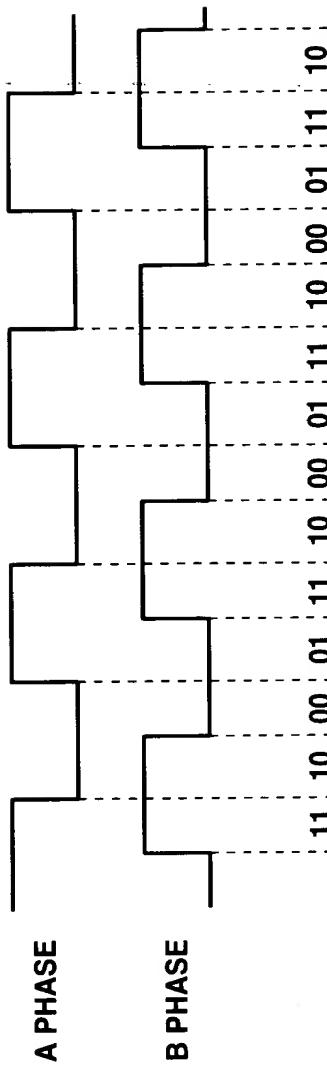


FIG.4B



FIG.4B

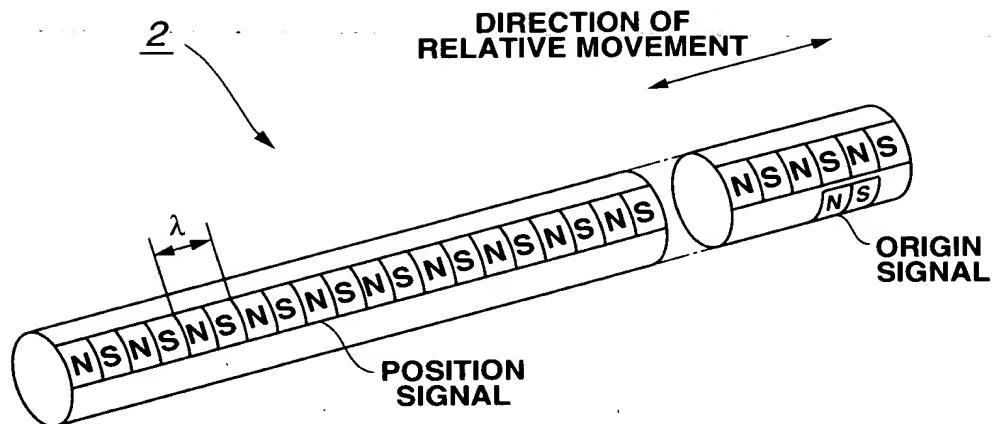


FIG.5

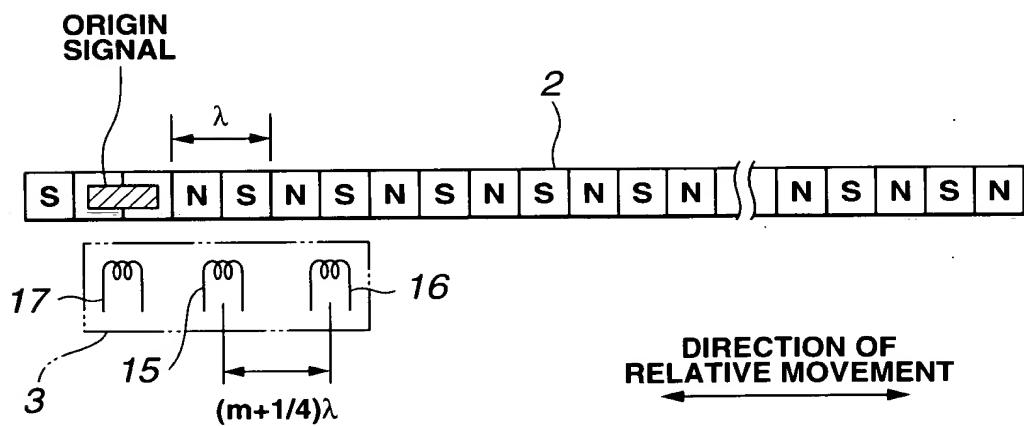


FIG.6

FIG.7A

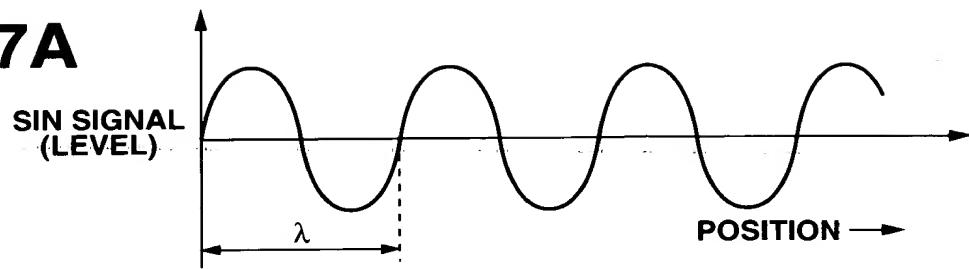


FIG.7B

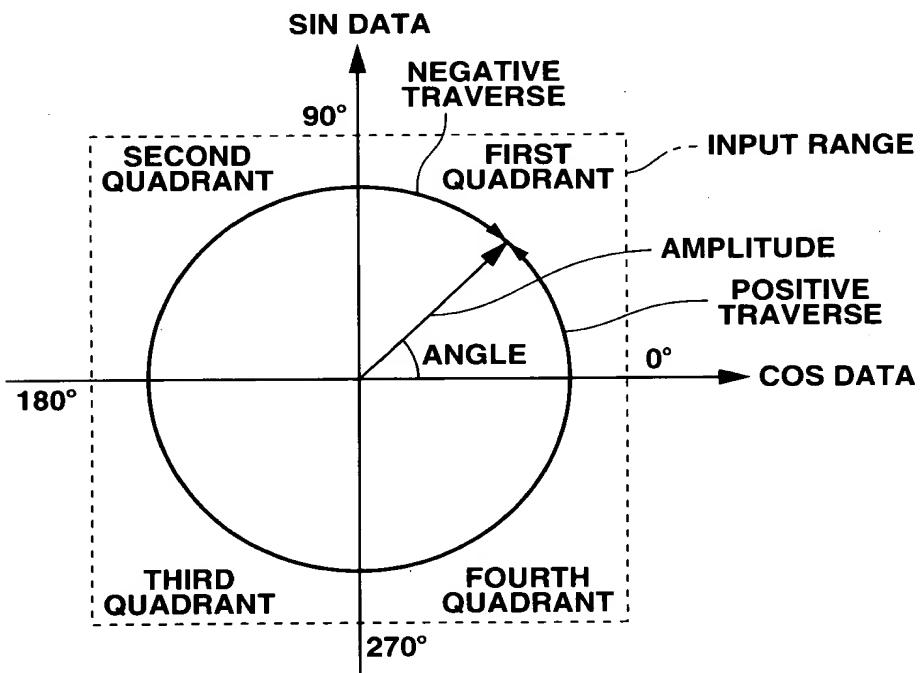
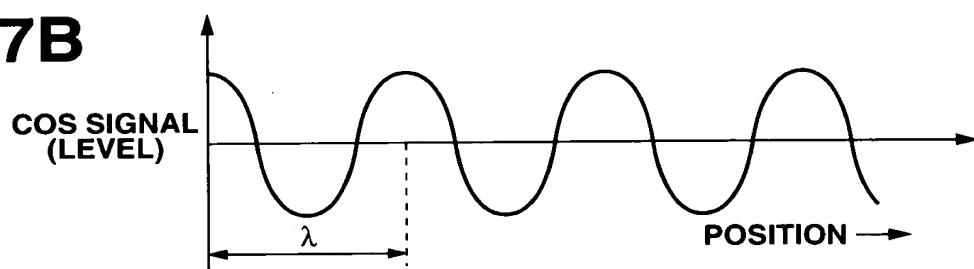


FIG.8

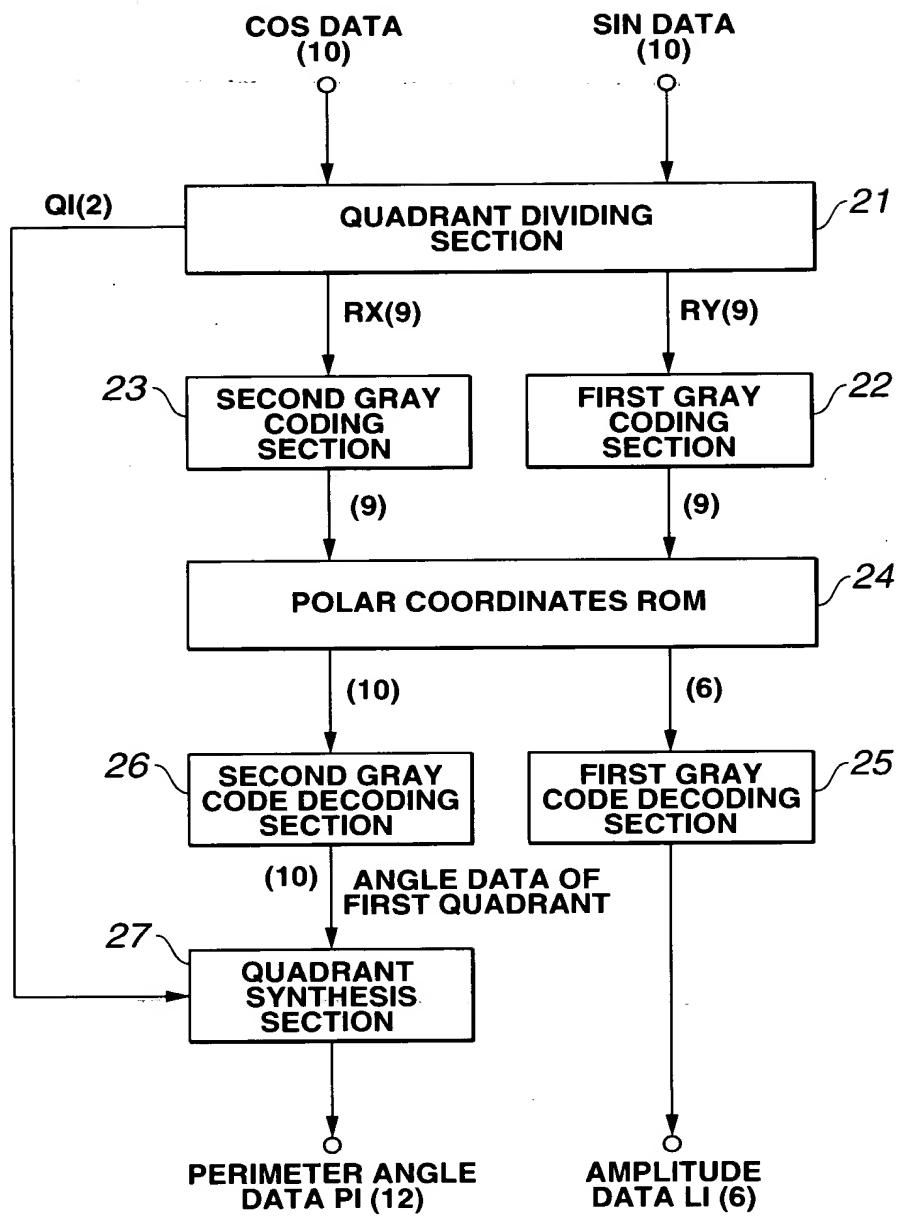


FIG.9

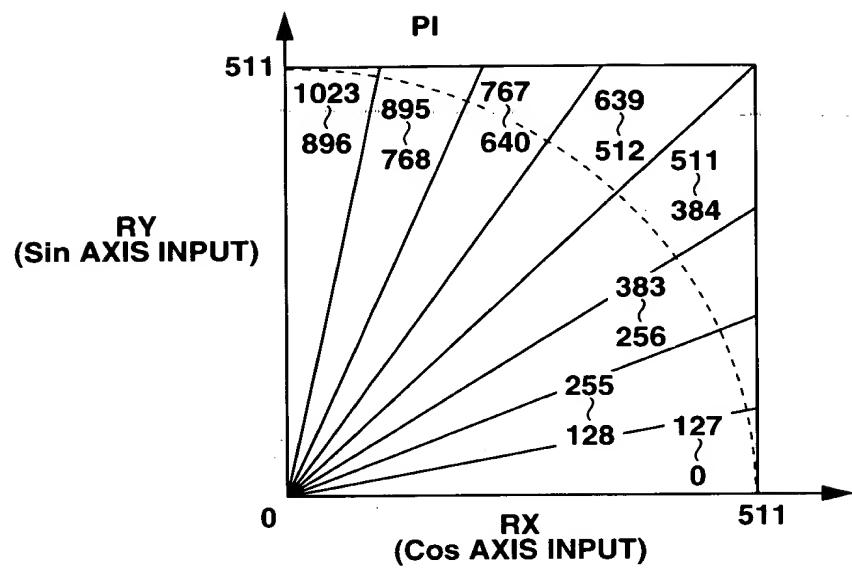


FIG.10

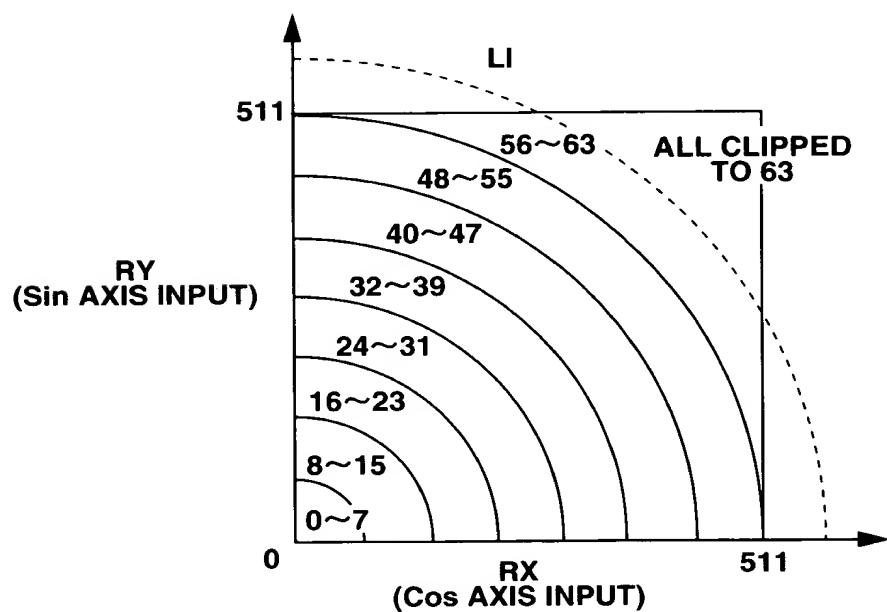


FIG.11

00649539 - 032600

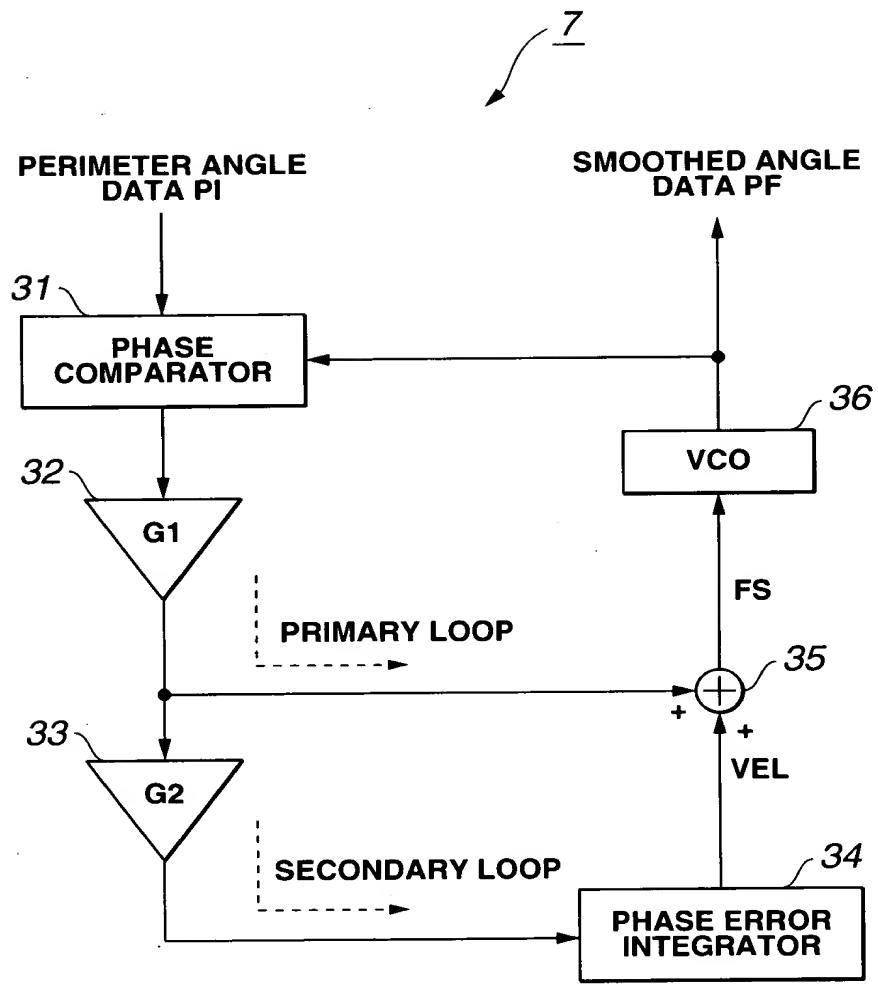


FIG.12

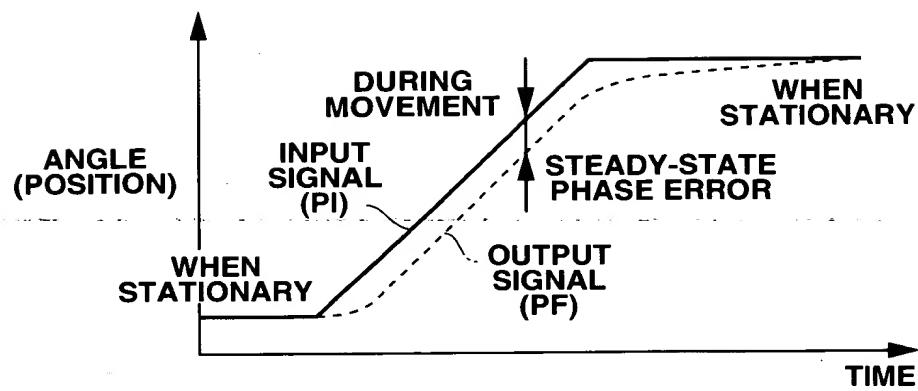


FIG.13

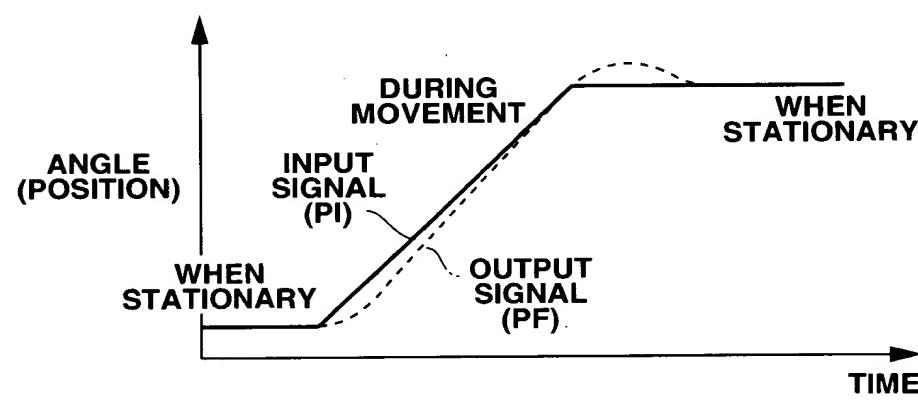


FIG.14

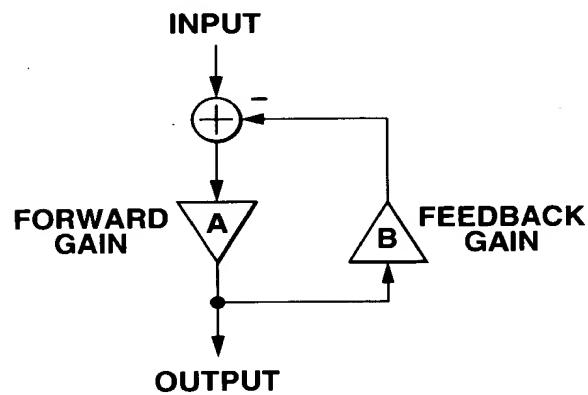


FIG.15

09649529-066800

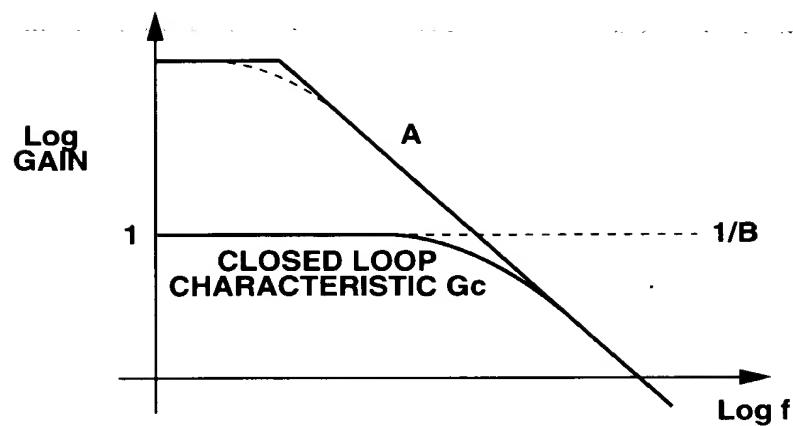


FIG.16

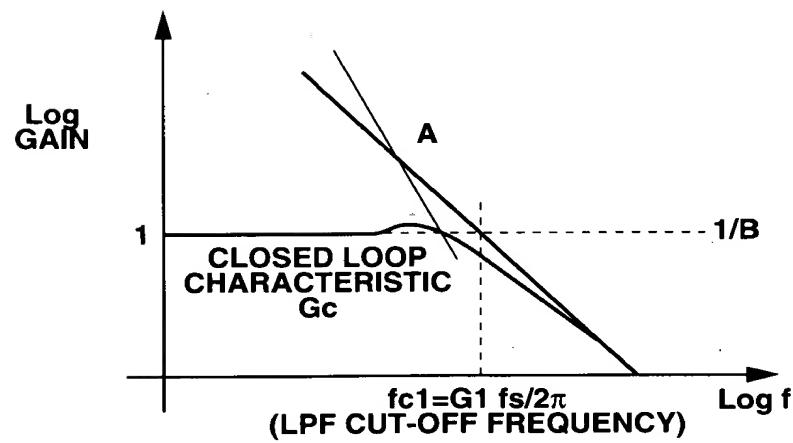


FIG.17

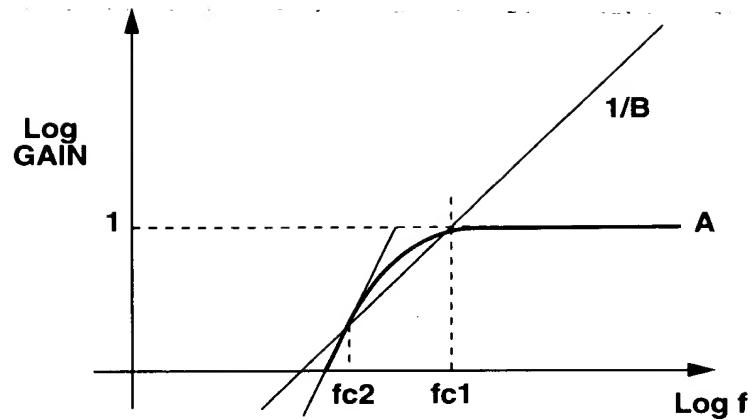


FIG.18

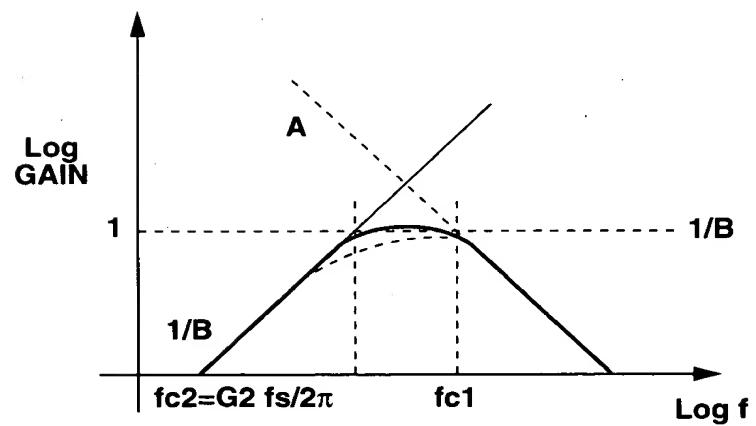


FIG.19

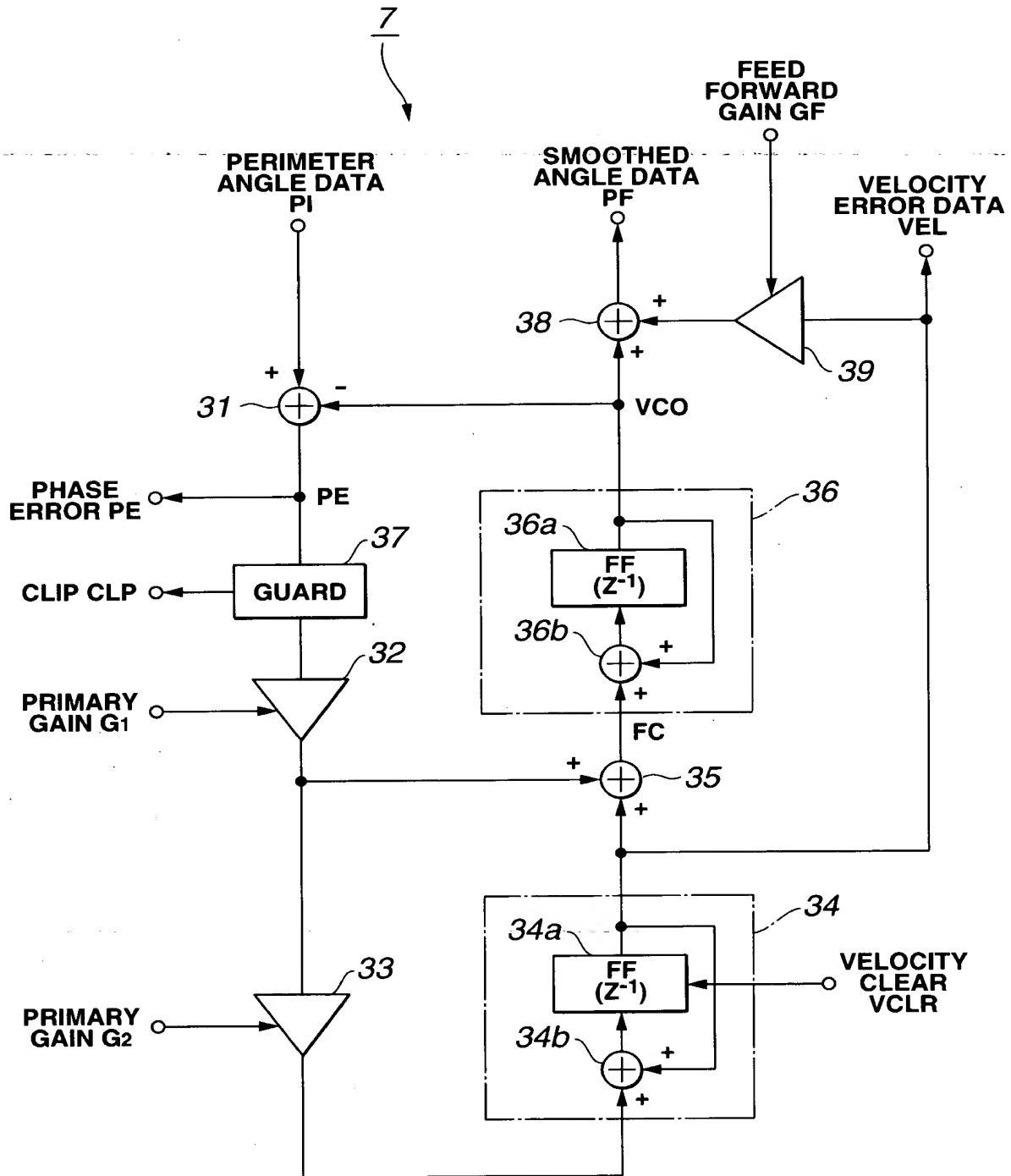


FIG.20

008260 564960

— OUTPUT SMOOTHED ANGLE DATA PF (FOR A RANGE OF FROM 0° TO 360°)
— MOVEMENT POSITION (PROPER POSITION, WITHOUT LIMITATION TO RANGE)
- - - INPUT SMOOTHED ANGLE DATA PI (FOR A RANGE OF FROM 0° TO 360°)

FIG.21A

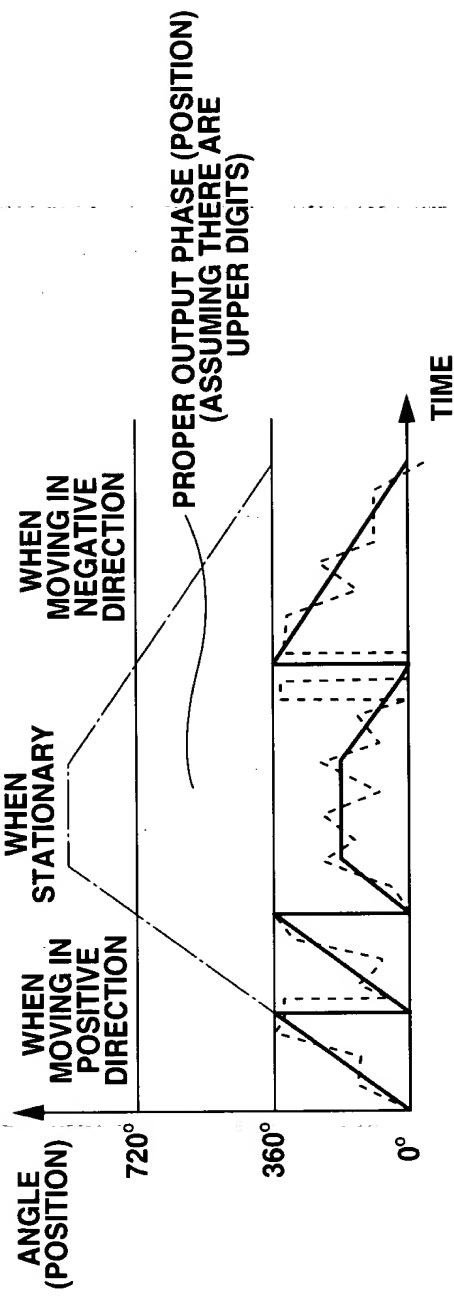
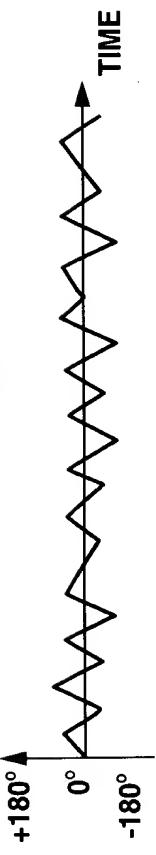


FIG.21B PHASE ERROR



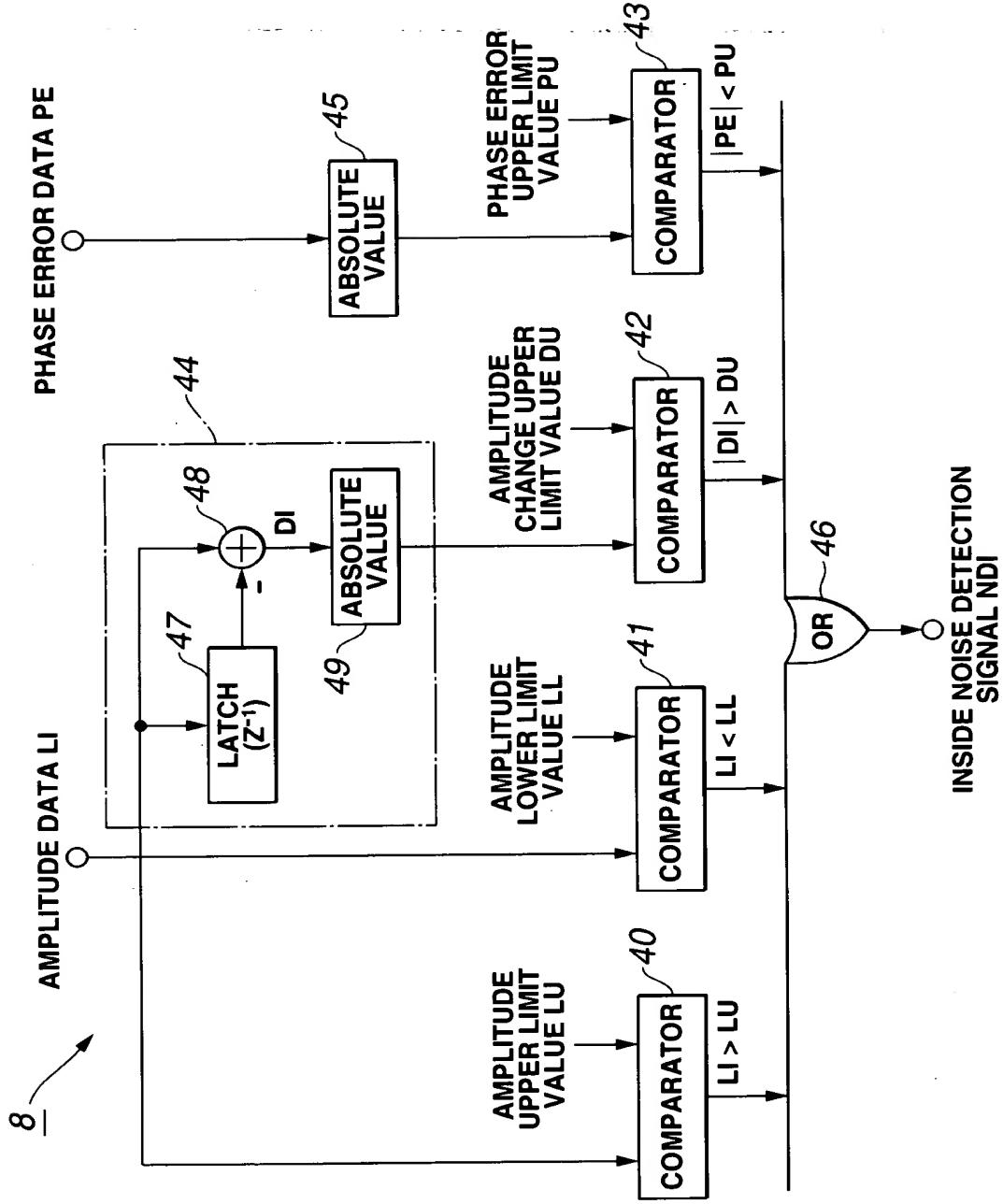


FIG.22

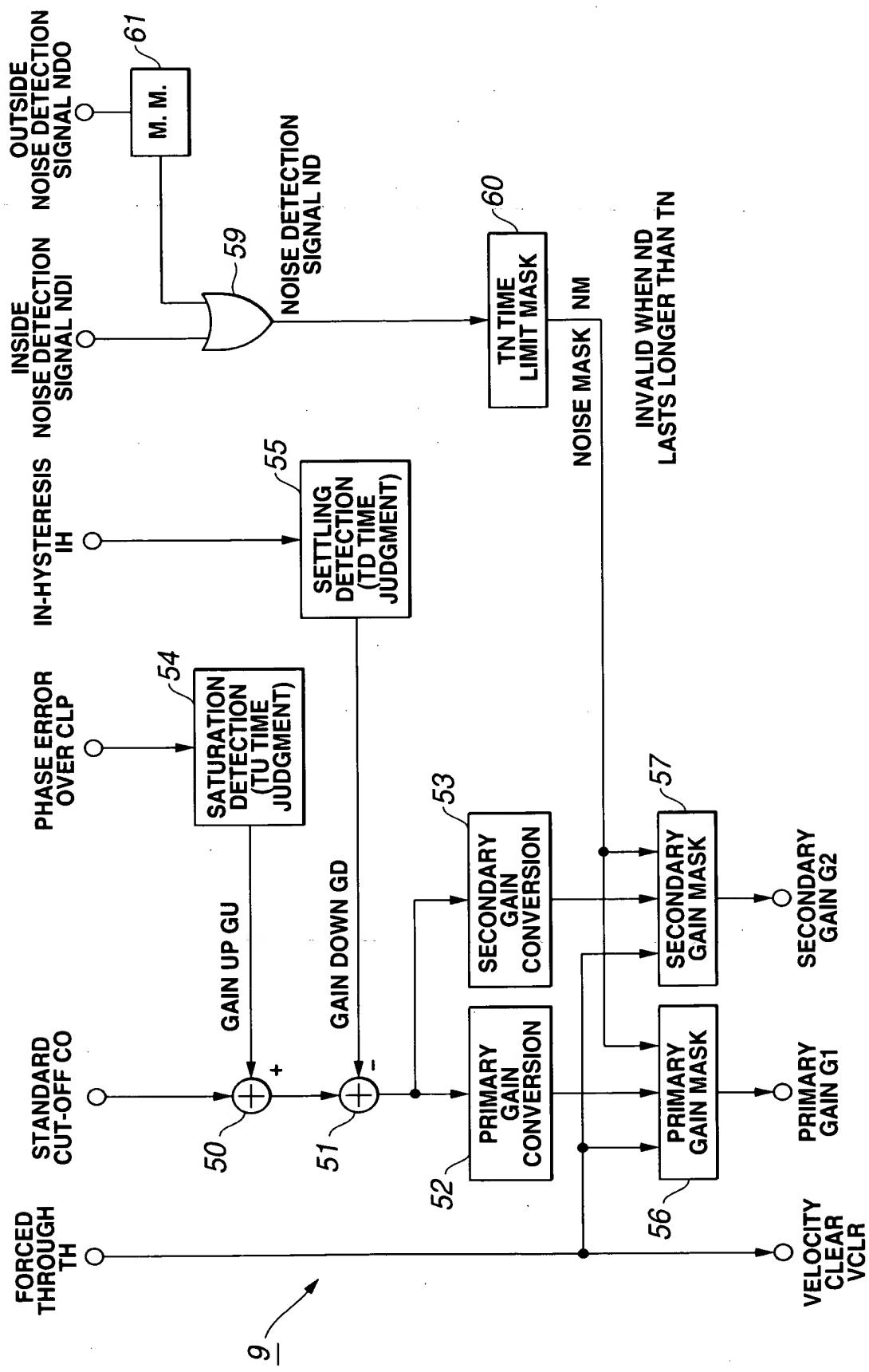


FIG.23

FIG.24A FORCED THROUGH TH
(VELOCITY CLEAR VCLR) THROUGH

NOISE DETECTION ND

NORMAL

FIG.24B IN-HYSTERESIS IH

PHASE ERROR OVER CLP

TD

FIG.24C GAIN UP GU

GAIN DOWN GD

TN

FIG.24D PRIMARY GAIN G1

SECONDARY GAIN G2

UP

FIG.24E GAIN DOWN GD

SECONDARY GAIN G2

DOWN

FIG.24F A1

A1/2

A1

FIG.24G A1

A1/2

A1

FIG.24H A1

A1/2

A1

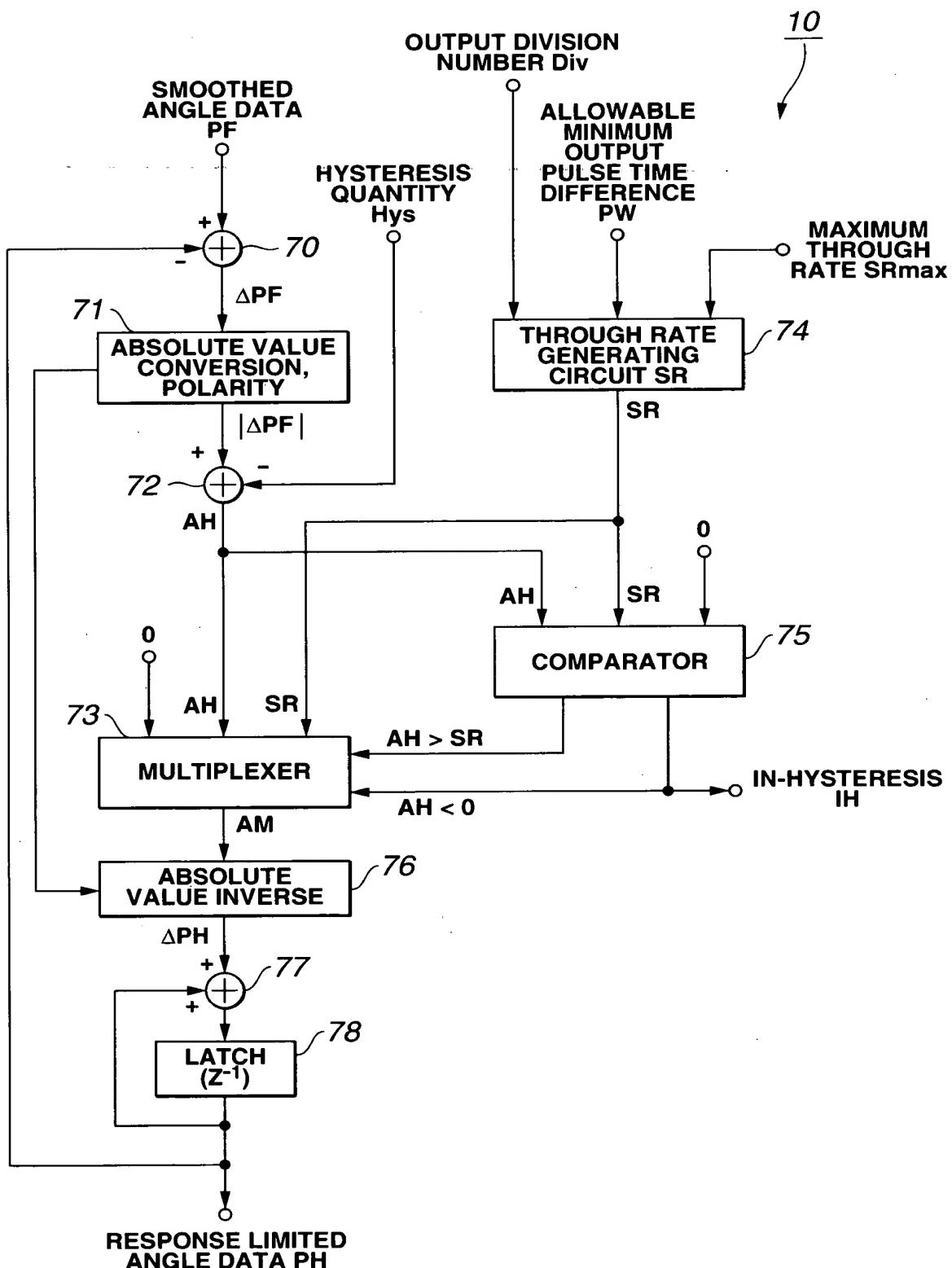


FIG.25

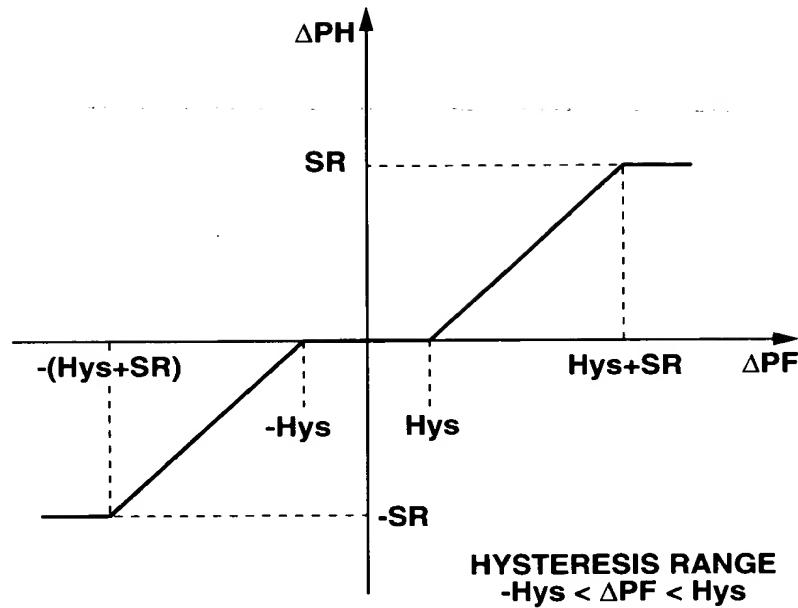


FIG.26

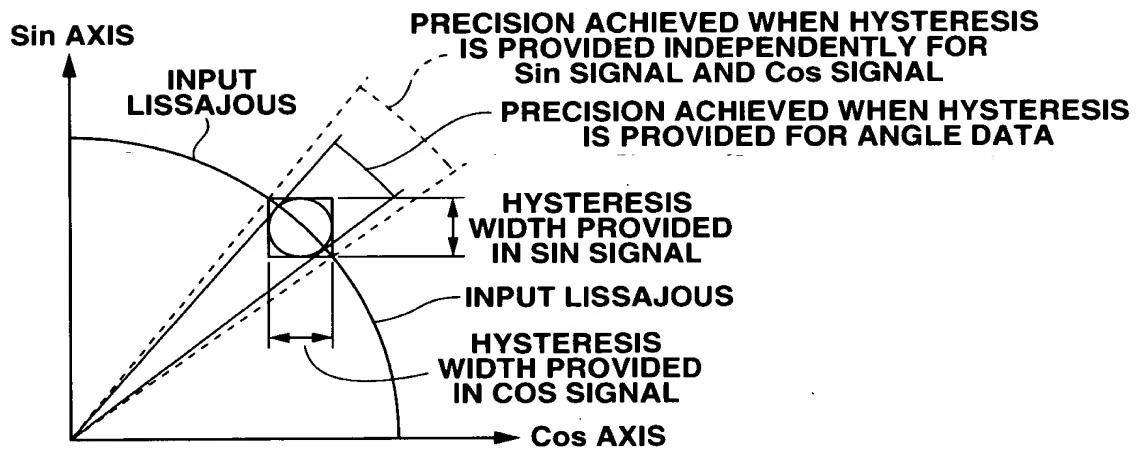


FIG.27

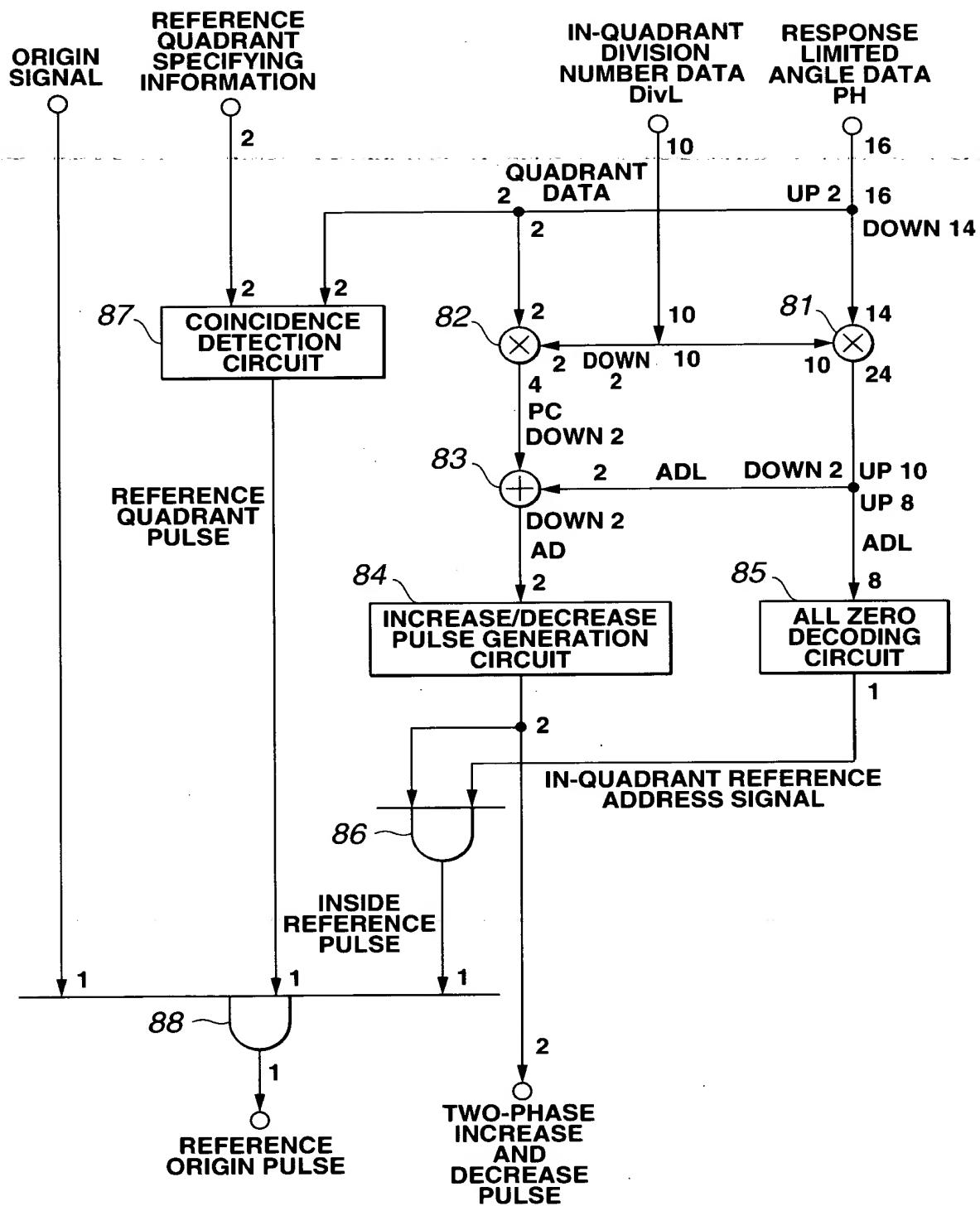


FIG.28

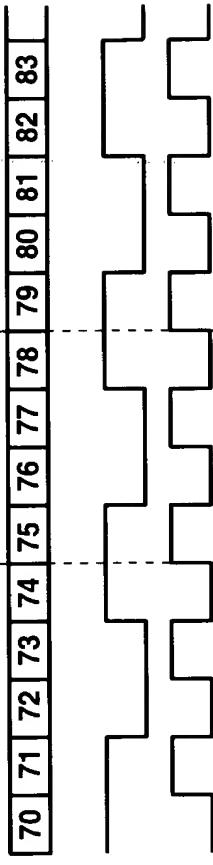
FIG.29AIN-QUADRANT
ADDRESS ADL

20	21	22	23	24	0	1	2	3	4	5	6	7	8
----	----	----	----	----	---	---	---	---	---	---	---	---	---

ADDRESS IN ONE
WAVELENGTH AD

70	71	72	73	74	75	76	77	78	79	80	81	82	83
----	----	----	----	----	----	----	----	----	----	----	----	----	----

SECOND BIT AD1 FROM
THE BOTTOM IN ADDRESS
IN ONE WAVELENGTH SAME
LEAST SIGNIFICANT BIT AD0

FIG.29BADDRESS IN ONE
WAVELENGTH AD**FIG.29C**

SECOND BIT AD1 FROM
THE BOTTOM IN ADDRESS
IN ONE WAVELENGTH SAME
LEAST SIGNIFICANT BIT AD0

TWO-PHASE
INCREASE AND
DECREASE PULSE {
A PHASE
SIGNAL
B PHASE
SIGNAL

FIG.29D

TWO-PHASE
INCREASE AND
DECREASE PULSE {
A PHASE
SIGNAL
B PHASE
SIGNAL

IN-QUADRANT ADDRESS ADL
EXCLUDING LOWER 2 BITS

FIG.29E

5	6	0	1	2
---	---	---	---	---

IN-QUADRANT
REFERENCE
ADDRESS SIGNAL

FIG.29F

INSIDE REFERENCE PULSE

FIG.29G

000280 " GE561960

IN-QUADRANT DIVISION DivL	SECOND MULTIPLIER INPUT	FIRST MULTIPLIER INPUT	SECOND MULTIPLIER INPUT	FIRST MULTIPLIER INPUT	RESPONSE LIMITED ANGLE DATA PH	DECIMAL POINT POSITION PER UNIT OF WAVELENGTH	DECIMAL POINT POSITION PER UNIT OF QUADRANT
(DivL9)	DivL9		PH15	PH14	PH15	PH13	
(DivL8)	DivL8		PH14		PH12	PH13	
(DivL7)	DivL7			PH13	PH12	PH12	
(DivL6)	DivL6			PH12	PH11	PH11	
(DivL5)	DivL5			PH11	PH10	PH10	
(DivL4)	DivL4			PH10	PH9	PH9	
(DivL3)	DivL3				PH8	PH8	
(DivL2)	DivL2				PH7	PH7	
DivL1	DivL1				PH6	PH6	
DivL0	DivL0				PH5	PH5	
					PH4	PH4	
					PH3	PH3	
					PH2	PH2	
					PH1	PH1	
					PH0	PH0	

FIG.30A

FIG.30B

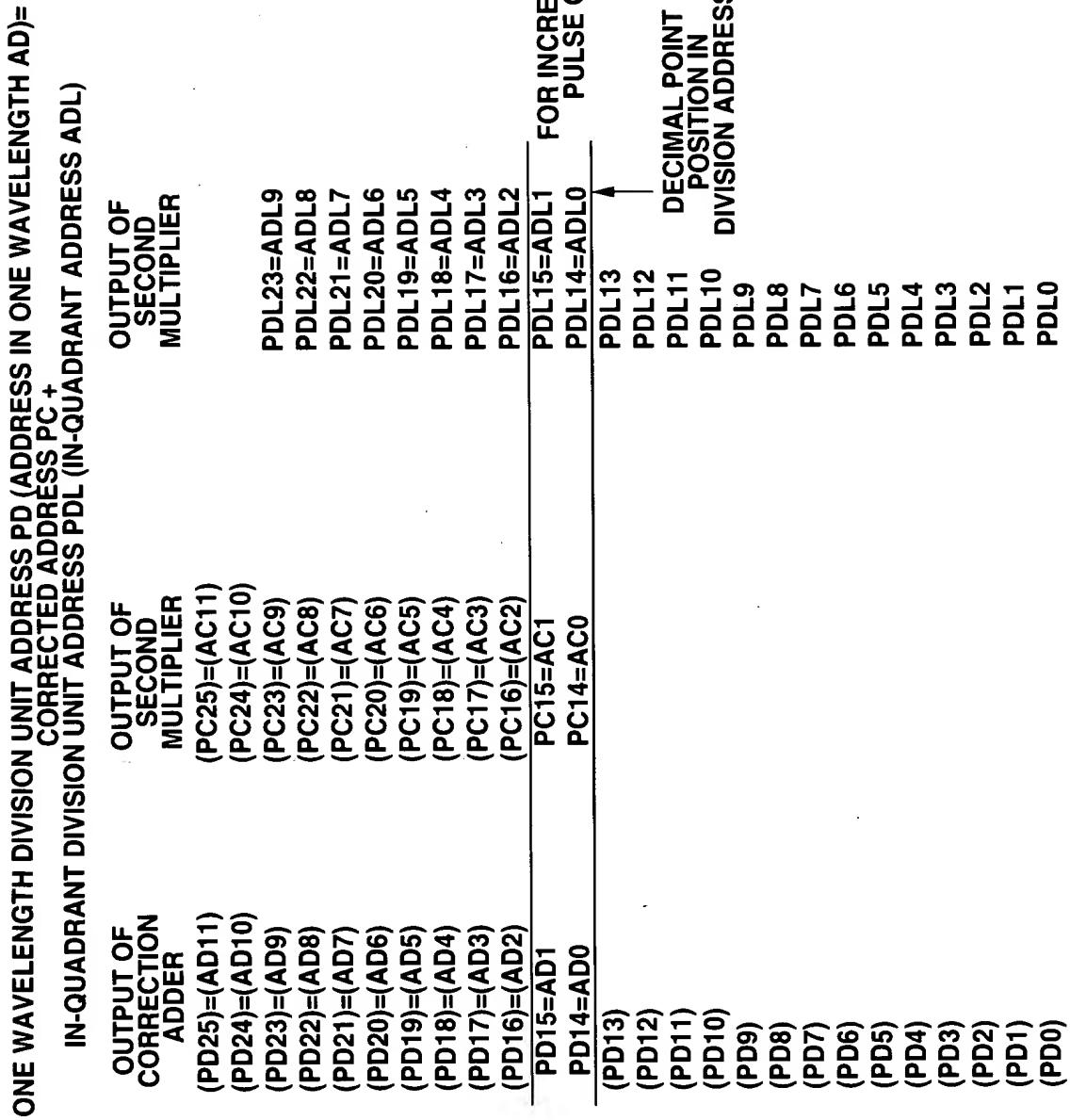


FIG.31

